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FIGURE 1A
MAIN

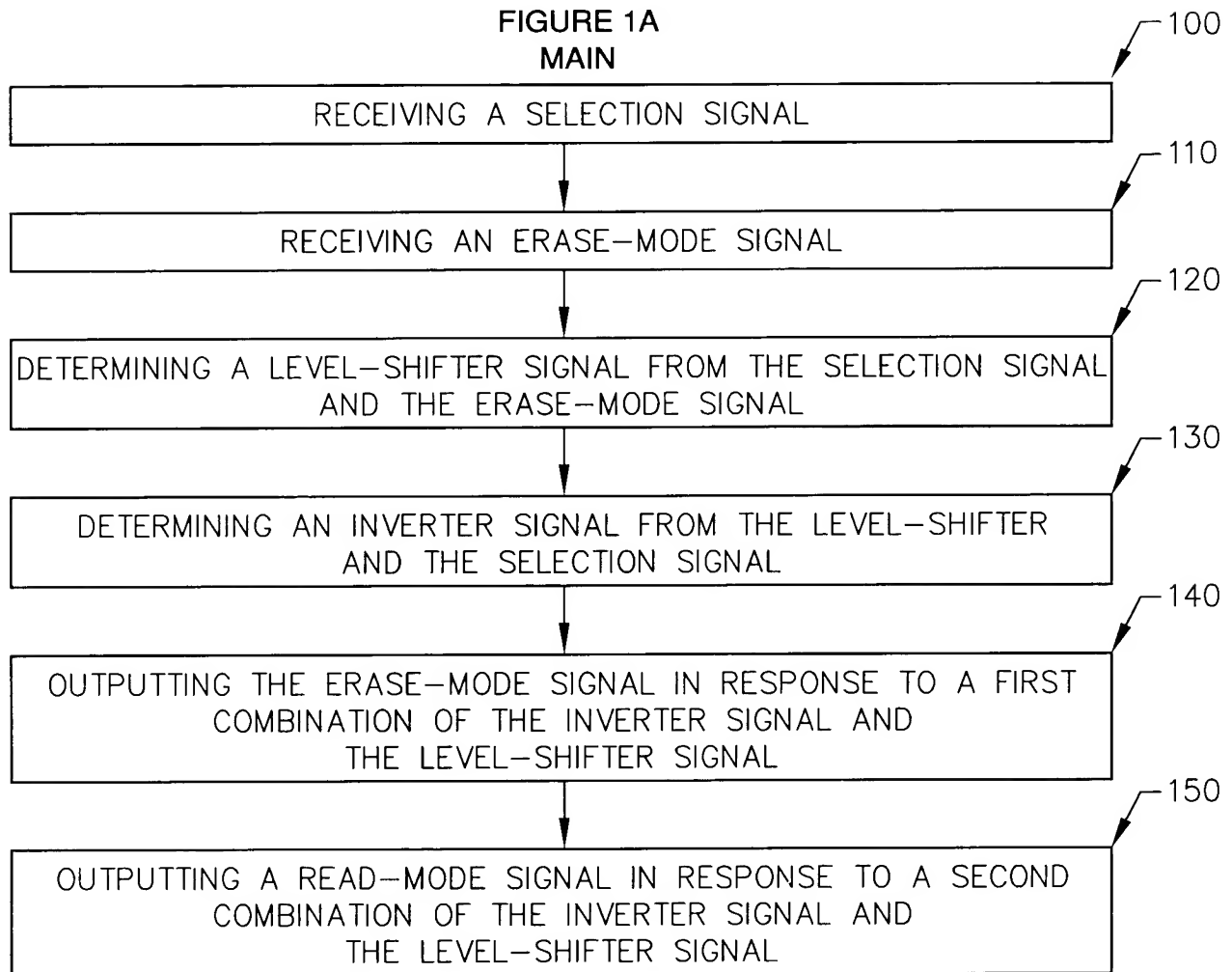


FIGURE 1B
RECEIVING A SELECTION SIGNAL

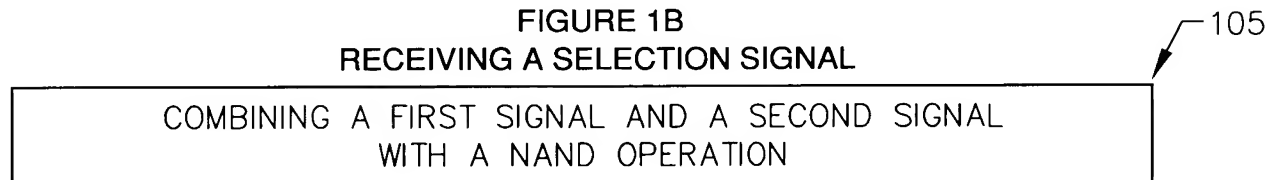
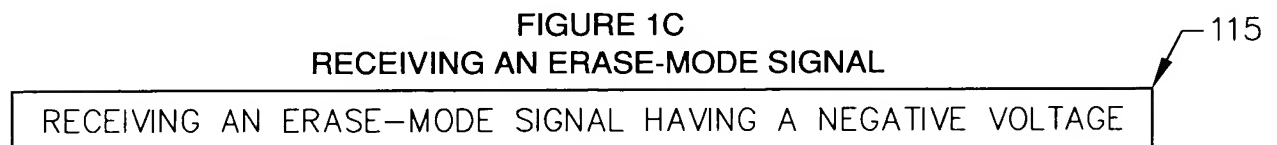


FIGURE 1C
RECEIVING AN ERASE-MODE SIGNAL



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FIGURE 1D
DETERMINING A LEVEL-SHIFTER SIGNAL FROM THE SELECTION
SIGNAL AND THE ERASE-MODE SIGNAL

PULLING UP A VOLTAGE FROM A NEGATIVE ERASE-MODE VOLTAGE

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FIGURE 1E
DETERMINING AN INVERTER SIGNAL FROM THE LEVEL-SHIFTER SIGNAL
AND THE SELECTION SIGNAL

CHOOSING BETWEEN THE LEVEL-SHIFTER SIGNAL
AND THE SELECTION SIGNAL

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FIGURE 1F
OUTPUTTING THE ERASE-MODE SIGNAL IN RESPONSE TO A FIRST
COMBINATION OF THE INVERTER SIGNAL AND
THE LEVEL-SHIFTER SIGNAL

OUTPUTTING THE ERASE-MODE SIGNAL IN RESPONSE TO A POSITIVE
VOLTAGE INVERTER SIGNAL AND A NEGATIVE VOLTAGE
LEVEL-SHIFTER SIGNAL

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FIGURE 1G
OUTPUTTING A READ-MODE SIGNAL IN RESPONSE TO A SECOND
COMBINATION OF THE INVERTER SIGNAL AND
THE LEVEL-SHIFTER SIGNAL

OUTPUTTING THE READ-MODE SIGNAL IN RESPONSE TO A NEGATIVE
VOLTAGE INVERTER SIGNAL AND A NEGATIVE VOLTAGE
LEVEL-SHIFTER SIGNAL

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FIGURE 2A
MAIN

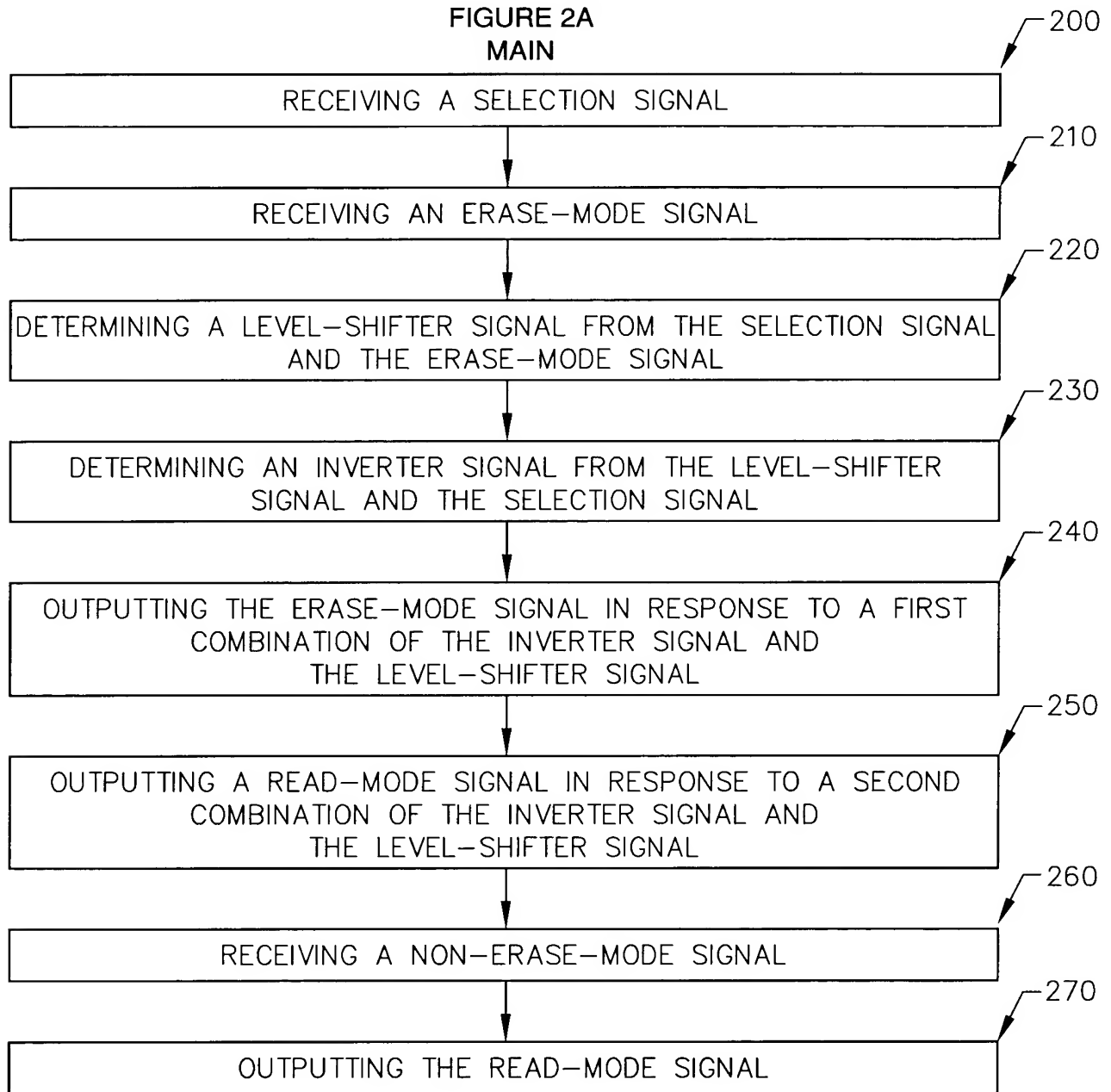
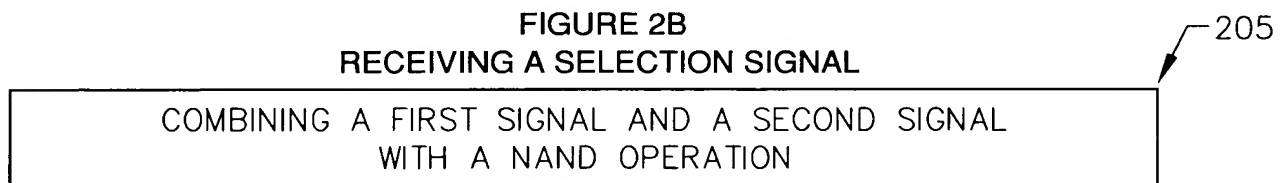


FIGURE 2B
RECEIVING A SELECTION SIGNAL



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FIGURE 2C
RECEIVING AN ERASE-MODE SIGNAL

RECEIVING AN ERASE-MODE SIGNAL HAVING A NEGATIVE VOLTAGE

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FIGURE 2D
DETERMINING A LEVEL-SHIFTER SIGNAL FROM THE SELECTION SIGNAL
AND THE ERASE-MODE SIGNAL

PULLING UP A VOLTAGE FROM A NEGATIVE ERASE-MODE VOLTAGE

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FIGURE 2E
DETERMINING AN INVERTER SIGNAL FROM THE LEVEL-SHIFTER SIGNAL
AND THE SELECTION SIGNAL

CHOOSING BETWEEN THE LEVEL-SHIFTER SIGNAL
AND THE SELECTION SIGNAL

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FIGURE 2F
OUTPUTTING THE ERASE-MODE SIGNAL IN RESPONSE TO A FIRST
COMBINATION OF THE INVERTER SIGNAL AND
THE LEVEL-SHIFTER SIGNAL

OUTPUTTING THE ERASE-MODE SIGNAL IN RESPONSE TO A POSITIVE
VOLTAGE INVERTER SIGNAL AND A NEGATIVE VOLTAGE
LEVEL-SHIFTER SIGNAL

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FIGURE 2G
OUTPUTTING A READ-MODE SIGNAL IN RESPONSE TO A SECOND
COMBINATION OF THE INVERTER SIGNAL AND
THE LEVEL-SHIFTER SIGNAL

OUTPUTTING THE READ-MODE SIGNAL IN RESPONSE TO A NEGATIVE
VOLTAGE INVERTER SIGNAL AND A NEGATIVE VOLTAGE
LEVEL-SHIFTER SIGNAL

255

FIGURE 2H
OUTPUTTING THE READ-MODE SIGNAL

SUPPLYING A LOW IMPEDANCE, LOW VOLTAGE CURRENT

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FIGURE 3

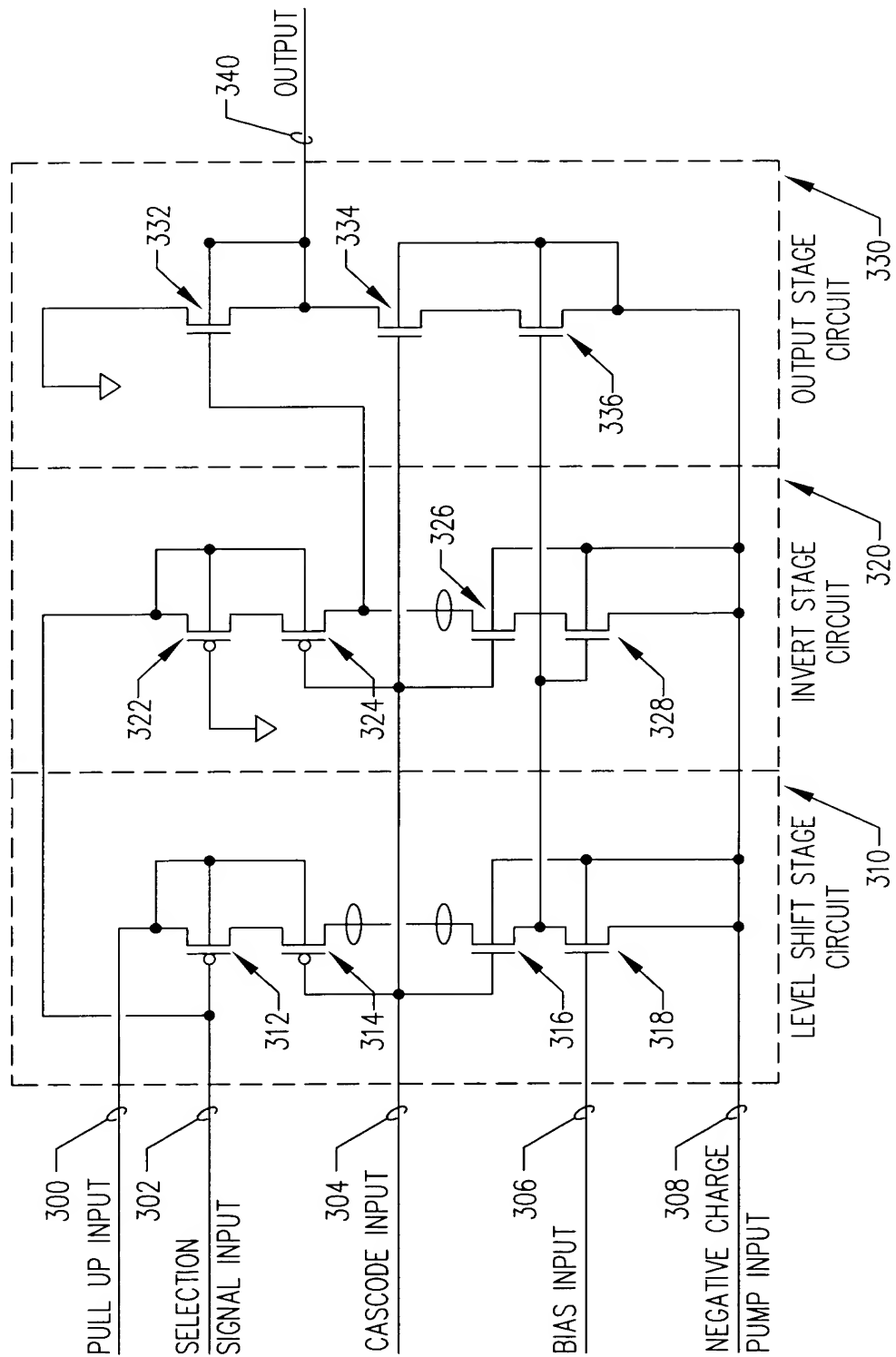


FIGURE 4

